創意電子
GLOBAL UNICHIP CORP.

UADC-8010-130

0.13μm 3.3V/1.2V 10-bit 400Ksps ADC [8ch]

Features

- **0.13μm 1.2V/3.3V logic salicide CMOS process with 1P5M layout**
- **Dual supply voltage (3.3V for analog; 1.2V for digital)**
- **Rail-to-rail input range**
- **Monotonic guaranteed**
- **8 channels input**
- **Operation temperature range:0℃~75℃**
- **Power down mode available**
- **Test chip available in LQFP-64 package**

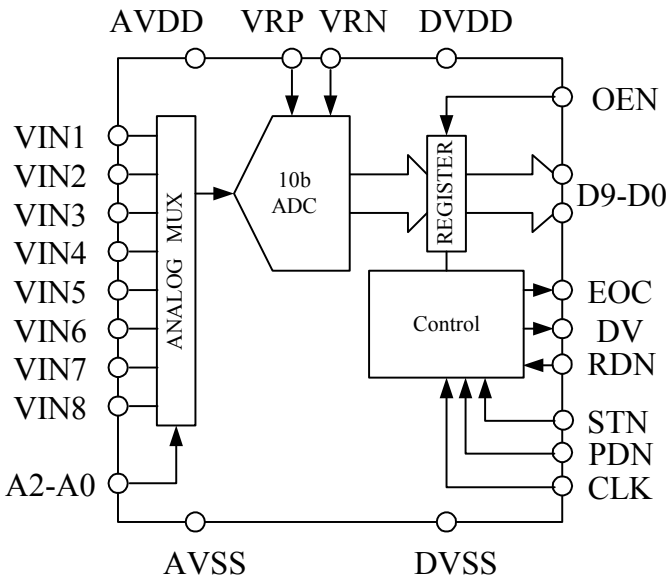
Applications

- **Hardware monitoring or fan speed control**
- **Battery voltage detection**
- **Voice recording**
- **Microprocessor peripheral**

Overview

UADC-8010-130 is a general-purpose analog to digital converter (ADC) with 8 multiplexed analog input channels. The architecture of ADC is successive approximation register (SAR) type. A power down mode is available with this IP core.

Block Diagram



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Description

The UADC-8010-130 is a 10-bit analog to digital converter with throughput of 400K samples per second. The analog input voltage was compared to external reference voltages VRP and VRN, and then converted to unsigned binary digital code. There is a set of tri-state output register store the converted digital code. The asynchronous output interface is compatible with most microprocessors.

Deliverables

- Comprehensive document set
- Hard macro
- Synopsys™ synthesis model
- Verilog model
- TLF model
- LEF model
- Testchip and evaluation board

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